

REMARKS/ARGUMENTS

Applicant has carefully reviewed and considered the Office Action mailed on August 22, 2005, and the references cited therewith.

Claims 1-20 are withdrawn. Applicant reserves the right to reintroduce these claims in a divisional or continuation application.

Claims 21, 25-26, 33, 41, 49, and 53 are amended, claims 36 and 45 are canceled, and no claims are added; as a result, claims 1-35, 37-44, and 46-57 are now pending in this application, where claims 1-20 were withdrawn.

Objection to the Specification

The Examiner objected to the Specification because element 110 was not shown in Figure 2 as indicated. Accordingly, Applicant has corrected paragraph [0007] of the specification to read, “In an embodiment described with reference to Fig. 1, the control signal 110”. Element 110 is provided in Figure 1 and accordingly, Applicant respectfully requests that the Examiner reconsider and withdraw the objection to the specification.

§102 Rejection of the Claims

Claims 21, 24, 25, 30, 32, 33, 35, 38, 40, 41, 43, 44, 47-49, 51-55, and 57 were rejected under 35 USC §102(b) as being anticipated by Wade, et al. (U.S. Patent No. 5,526,027).

The Wade, et al. reference appears to teach an ink jet printer that includes a controller, a printhead driver circuit, and a controlled voltage power supply to provide the driver circuit with a controlled supply voltage whose magnitude is controlled by the controller (Col. 2, lines 58-65; Fig. 2).

In contrast, Applicant’s independent claim 21, as amended, recites, among other things:

an integrated circuit configured to generate a pulse width modulated control signal, the integrated circuit configured external to the power supply; and

a voltage adjustment circuit configured to receive the pulse width modulated control signal and generate a difference voltage to adjust the voltage output of the power supply, wherein

the adjustment circuit includes an integrator circuit configured to generate the difference voltage, and wherein the integrator circuit includes a buffer circuit configured to receive the pulse width modulated

control signal, and includes a DC filter configured to filter the pulse width modulated control signal.

Applicant's independent claim 33, as amended, recites, among other things:

generating a difference signal according to the control signal to adjust the output of the power supply, wherein generating the difference signal includes:

buffering the control signal with a buffer circuit; and
filtering the control signal with a DC filter to generate the difference signal that varies a feedback signal to the power supply.

Furthermore, Applicant's independent claim 41, as amended, recites, among other things:

generating a pulse width modulated control signal for input to a voltage adjustment circuit; and

generating a difference voltage with the voltage adjustment circuit to adjust the voltage output of the power supply, wherein generating the difference voltage includes:

buffering the pulse width modulated control signal with a buffer circuit; and

filtering the pulse width modulated control signal with a DC filter to generate the difference voltage to vary a feedback voltage to the power supply.

Moreover, Applicant's independent claim 49, as amended, recites, among other things:

generate a control signal for input to an adjustment circuit, the control signal configured to be generated external to the power supply; and

generate a difference signal according to the control signal to adjust the output of the power supply, wherein the instructions that execute to generate the difference signal also execute to:

buffer the control signal with a buffer circuit; and

filter the control signal with a DC filter to generate the difference signal to vary a feedback voltage to the power supply.

Applicant's independent claim 53, as amended, recites, among other things:

means to generate a pulse width modulated control signal for input to a voltage adjustment circuit that generates a difference voltage, wherein the voltage adjustment circuit includes means to buffer the control signal and means to filter the control signal to generate the difference voltage to vary a feedback voltage to the power supply; and

means to adjust the voltage output of the power supply based upon the difference voltage and the voltage output.

The Wade, et al. reference does not teach the elements of the independent claims recited above. For example, the Wade, et al. reference does not teach that “the adjustment circuit includes an integrator circuit configured to generate the difference voltage, and wherein the integrator circuit includes a buffer circuit configured to receive the pulse width modulated control signal, and includes a DC filter configured to filter the pulse width modulated control signal” as recited in Applicant’s independent claim 21, as amended.

As such, each and every element and limitation of independent claims 21, 33, 41, 49, and 53, as amended, are not taught or suggested by the Wade, et al. reference. Accordingly, Applicant respectfully requests reconsideration and withdrawal of the §102 rejection of independent claims 21, 33, 41, 49, and 53 as well as all claims that depend therefrom.

§103 Rejection of the Claims

Claim 22 was rejected under 35 USC §103(a) as being unpatentable over Wade, et al. (U.S. Patent No. 5,526,027) in view of Chang, et al. (U.S. Patent No. 5,541,628). Claim 22 depends from independent claim 21. For the reasons provided above, the Applicant believes that independent claim 21, as amended, is allowable over the Wade, et al. reference. The Chang, et al. reference does not cure the deficiencies of the Wade, et al. reference. Accordingly, the Applicant respectfully requests reconsideration and withdrawal of the §103 rejection of dependent claim 22.

Claims 23, 34, 42, 50, and 56 were rejected under 35 USC §103(a) as being unpatentable over Wade, et al. (U.S. Patent No. 5,526,027). Claims 23, 34, 42, 50, and 56 depend from one of independent claims 21, 33, 41, 49, and 53. For the reasons provided above, the Applicant believes that independent claims 21, 33, 41, 49, and 53, as amended, are allowable over the Wade, et al. reference. Accordingly, the Applicant respectfully requests reconsideration and withdrawal of the §103 rejection of dependent claims 23, 34, 42, 50, and 56.

Claims 26-29, 36, 37, 45, and 46 were rejected under 35 USC §103(a) as being unpatentable over Wade, et al. (U.S. Patent No. 5,526,027) in view of Ivankovic (U.S. Publ. No. 2004/0223271). Claims 26-29, 36, 37, 45, and 46 depend from one of independent claims 21, 33, and 41. For the reasons provided above, the Applicant believes that independent claims 21, 33, and 41, as

amended, are allowable over the Wade, et al. reference. The Ivankovic reference does not cure the deficiencies of the Wade, et al. reference. Furthermore, the Applicant respectfully submits that even if combined, the Wade, et al. and the Ivankovic references do not teach or suggest all of the elements of Applicant's claimed invention. Accordingly, the Applicant respectfully requests reconsideration and withdrawal of the §103 rejection of dependent claims 26, 27, 28, 29, 37, and 46.

Claims 31 and 39 were rejected under 35 USC §103(a) as being unpatentable over Wade, et al. (U.S. Patent No. 5,526,027) in view of Hancock, et al. (U.S. Patent No. 4,835,669). Claim 31 depends from independent claim 21 and claim 39 depends from independent claim 33. For the reasons provided above, the Applicant believes that independent claims 21 and 33, as amended, are allowable over the Wade, et al. reference. The Hancock, et al. reference does not cure the deficiencies of the Wade, et al. reference. Accordingly, the Applicant respectfully requests reconsideration and withdrawal of the §103 rejection of dependent claims 31 and 39.

CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney Gregg W. Wisdom at (360) 212-8052 to facilitate prosecution of this matter.

At any time during the pendency of this application, please charge any additional fees or credit overpayment to the Deposit Account No. 08-2025.

CERTIFICATE UNDER 37 CFR §1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AMENDMENT Commissioner for Patents, P.O. BOX 1450, Alexandria, VA 22313-1450 on this 14th day of November, 2005.

Sarah L. Richard
Name
Sarah L. Richard
Signature

Respectfully Submitted,
Glenn M. Smith, et al.

By their Representatives,
BROOKS & CAMERON, PLLC
1221 Nicollet Avenue, Suite 500
Minneapolis, MN 55403

By: Edward J. Brooks III
Edward J. Brooks III
Reg. No. 40,925

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